NOVACOR, INC.

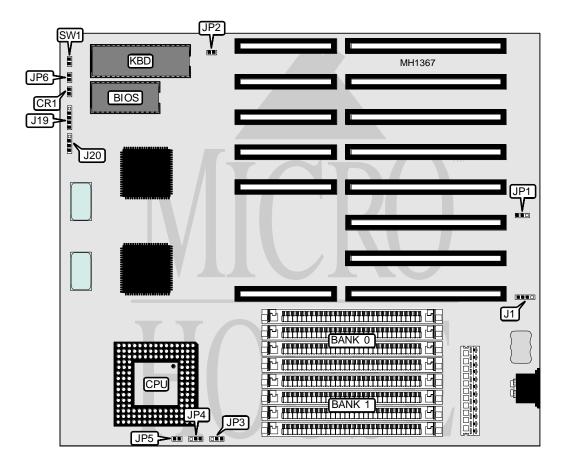
OPTIMUM 486DXBB - 4A4B1

80486SX/80486DX **Processor Processor Speed** 25/33/50MHz

Chip Set OPTI Max. Onboard DRAM 32MB **SRAM Cache** None **BIOS** AMI

254mm x 218mm **Dimensions**

I/O Options None **NPU Options** None



CONNECTIONS					
Purpose	Location	Purpose	Location		
Turbo LED	CR1	Speaker	J20		
External battery	J1	Turbo switch	JP6		
Power LED & keylock	J19	Reset switch	SW1		

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USER CONFIGURABLE SETTINGS				
Function	Jumper	Position		
í CMOS memory normal operation	JP1	pins 2 & 3 closed		
CMOS memory clear	JP1	pins 1 & 2 closed		
í Password select enabled	JP2	closed		
Password select disabled	JP2	open		

DRAM CONFIGURATION				
Size	Bank 0	Bank 1		
1MB	(4) 256K x 9	NONE		
2MB	(4) 256K x 9	(4) 256K x 9		
4MB	(4) 1M x 9	NONE		
5MB	(4) 256K x 9	(4) 1M x 9		
8MB	(4) 1M x 9	(4) 1M x 9		
16MB	(4) 4M x 9	NONE		
32MB	(4) 4M x 9	(4) 4M x 9		

CPU TYPE CONFIGURATION					
Type	JP3	JP4	JP5		
80486DX	pins 1 & 2 closed	pins 1 & 2 closed	closed		
80487SX	pins 2 & 3 closed	pins 1 & 2 closed	closed		
80486DX	open	pins 2 & 3 closed	open		