CSS LABORATORIES, INC.
LITTLE MAX 386SX

Processor	80386SX
Processor Speed	16/20MHz
Chip Set	C & T
Max. Onboard DRAM	16MB
SRAM Cache	None
BIOS	AMI
Dimensions	334mm x 127mm
I/O Options	Auxiliary keyboard, IDE interface, floppy drive interface, parallel port, serial ports(2)
NPU Options	80387SX
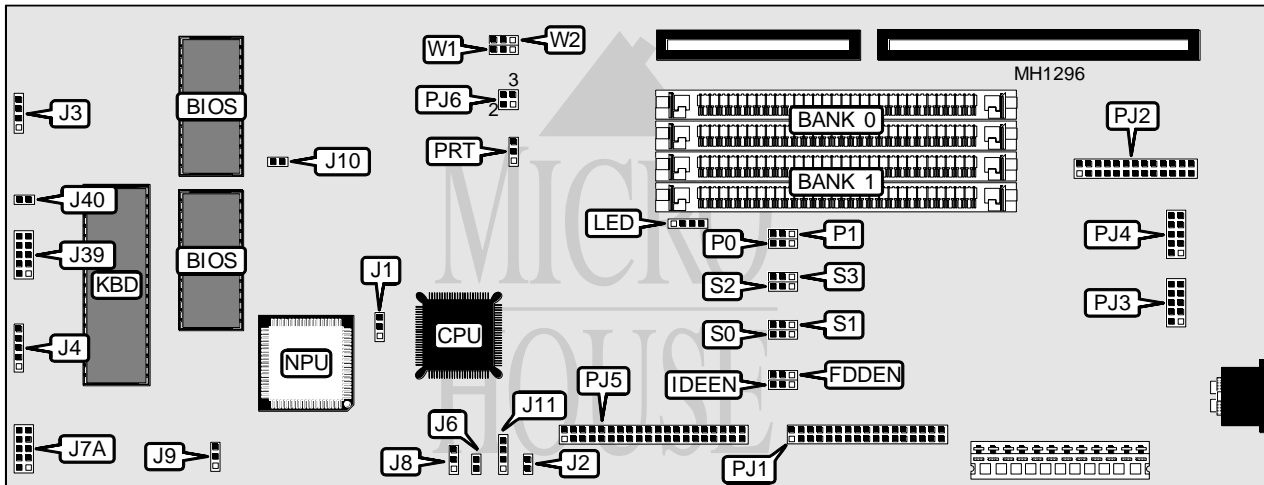
CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J3	Hard drive access LED	J40
Power LED & keylock	J4	IDE interface LED	LED
Turbo switch	J6	Floppy drive interface	PJ1
Auxiliary keyboard	J7A	Parallel port	PJ2
Reset switch	J11 pins 1 & 2	Serial port 1	PJ3
Turbo LED	J11 pins 3 & 4	Serial port 2	PJ4
Front panel switches & LEDs	J39	IDE interface	PJ5

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Floppy drive interface enabled	FDDEN	pins 2 & 3 closed
Floppy drive interface disabled	FDDEN	pins 1 & 2 closed
í IDE interface enabled	IDEEN	pins 2 & 3 closed
IDE interface disabled	IDEEN	pins 1 & 2 closed
í Factory configured - do not alter	J2	open
í Monitor type select color	J8	pins 2 & 3 closed
Monitor type select monochrome	J8	pins 1 & 2 closed
í BIOS type select 27256	J9	pins 1 & 2 closed
BIOS type select 27128	J9	pins 2 & 3 closed
CPU speed select 16MHz	J10	closed
CPU speed select 20MHz	J10	open
	J1	pins 2 & 3 closed
	W1	pins 1 & 2 closed
í Pipeline mode enabled	W2	pins 2 & 3 closed
	J1	pins 1 & 2 closed
	W1	pins 2 & 3 closed
Pipeline mode disabled	W2	pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(2) 256K x 9	NONE
1MB	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9
8MB	(2) 4M x 9	NONE
16MB	(2) 4M x 9	(2) 4M x 9

SERIAL PORT 1 CONFIGURATION					
COM	IRQ Interrupt	I/O Address	Jumper PJ6	S0	S1
COM 2	IRQ3	2F8h	pins 1 & 3	pins 1 & 2	pins 2 & 3
COM 1	IRQ4	3F8h	pins 1 & 2	pins 2 & 3	pins 2 & 3
Disabled	N/A	N/A	N/A	pins 1 & 2	pins 1 & 2

SERIAL PORT 2 CONFIGURATION					
COM	IRQ Interrupt	I/O Address	Jumper PJ6	S2	S3
COM 1	IRQ4	3F8h	pins 2 & 4	pins 1 & 2	pins 2 & 3
COM 2	IRQ3	2F8h	pins 3 & 4	pins 2 & 3	pins 2 & 3
Disabled	N/A	N/A	N/A	pins 1 & 2	pins 1 & 2

PARALLEL PORT 1 (PJ2) CONFIGURATION					
LPT	IRQ Interrupt	I/O Address	P0	P1	PRT
LPT 1	IRQ7	3F8h	pins 1 & 2	pins 2 & 3	pins 2 & 3
LPT 2	IRQ5	2F8h	pins 2 & 3	pins 2 & 3	pins 1 & 2
LPT 3	IRQ7	3BCh	pins 2 & 3	pins 1 & 2	pins 2 & 3
Disabled	N/A	N/A	pins 1 & 2	pins 1 & 2	N/A