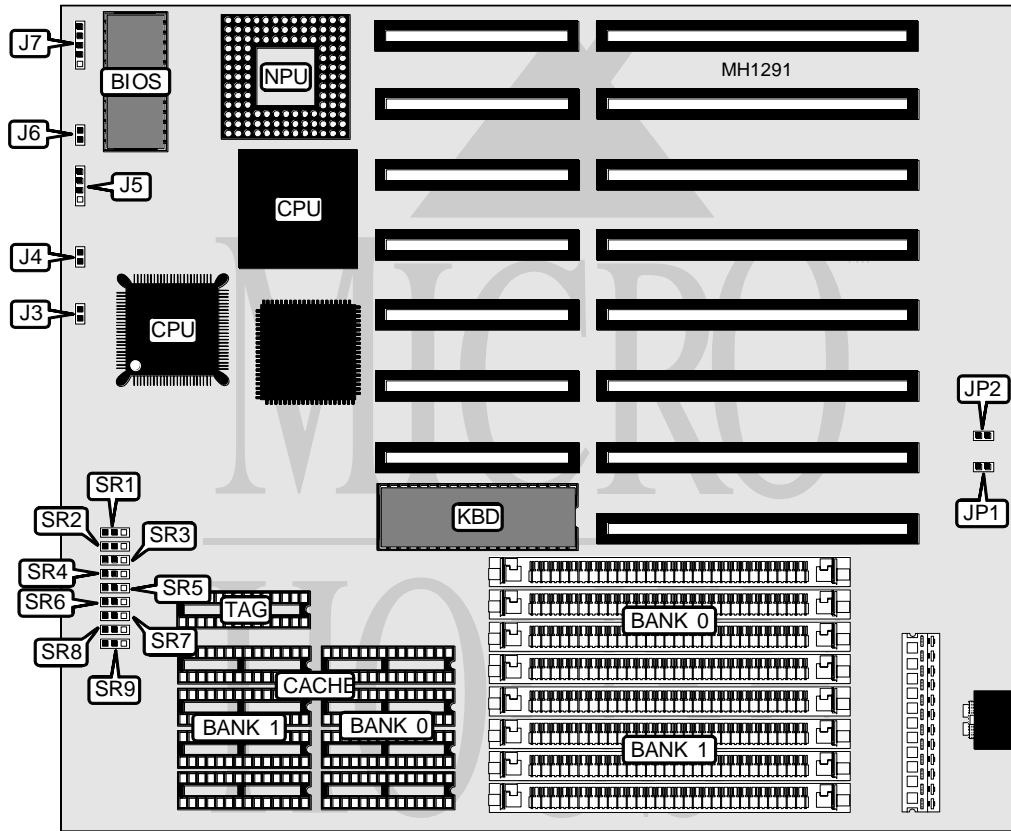


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386S14

Processor 80386DX/CX486DLC
Processor Speed 33/40MHz
Chip Set C & T
Max. Onboard DRAM 32MB
SRAM Cache 64/128/256KB
BIOS AMI
Dimensions 254mm x 218mm
I/O Options None
NPU Options 80387/CX83D87/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	J3	Reset switch	J6
Turbo switch	J4	Power LED & keylock	J7
Speaker	J5		

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USER CONFIGURABLE SETTINGS

Function	Jumper	Position
í Factory configured - do not alter	JP1	open
í Monitor type select color	JP2	closed
Monitor type select monochrome	JP2	open

DRAM CONFIGURATION

Size	Bank 0	Bank 1	JP11
1MB	(4) 256K x 9	NONE	pins 2 & 3 closed
2MB	(4) 256K x 9	(4) 256K x 9	pins 1 & 2 closed
4MB	(4) 1M x 9	NONE	pins 2 & 3 closed
5MB	(4) 1M x 9	(4) 256K x 9	pins 2 & 3 closed
8MB	(4) 1M x 9	(4) 1M x 9	pins 1 & 2 closed
16MB	(4) 4M x 9	NONE	pins 2 & 3 closed
17MB	(4) 4M x 9	(4) 256K x 9	pins 2 & 3 closed
20MB	(4) 4M x 9	(4) 1M x 9	pins 2 & 3 closed
32MB	(4) 4M x 9	(4) 4M x 9	pins 1 & 2 closed

SRAM JUMPER CONFIGURATION

Jumper	64KB	128KB	256KB
SR1	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed
SR2	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed
SR3	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
SR4	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed
SR5	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed
SR6	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
SR7	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed
SR8	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
SR9	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed

SRAM CONFIGURATION

Size	Cache SRAM	Location	TAG
64KB	(8) 8K x 8	Banks 0 & 1	(1) 8K x 8
128KB	(4) 32K x 8	Bank 0	(1) 8K x 8
256KB	(8) 32K x 8	Banks 0 & 1	(1) 8K x 8