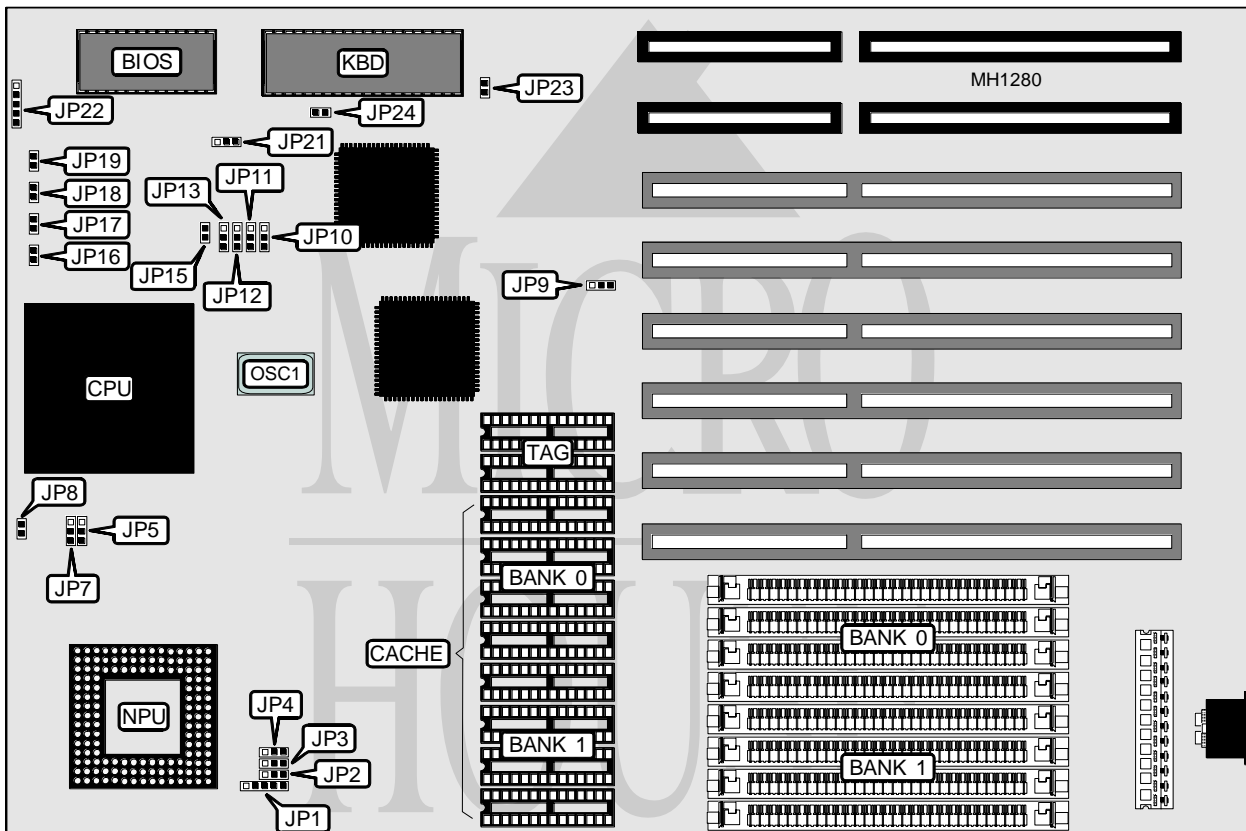


CAF TECHNOLOGY, INC.

E 4 2 6 E I S A

Processor	80486SX/80487SX/80486DX/80486DX2
Processor Speed	33/50(internal)/50/66(internal)MHz
Chip Set	SIS
Max. Onboard DRAM	128MB
SRAM Cache	64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	4167



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED	JP8	Reset switch	JP18
Turbo LED	JP16	Speaker	JP19
Turbo switch	JP17	Keylock	JP22

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E426 EISA

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP24	open
Monitor type select monochrome	JP24	closed
í Factory configured - do not alter	JP9	unknown
í Factory configured - do not alter	JP10	unknown
í Factory configured - do not alter	JP11	unknown
í Factory configured - do not alter	JP21	unknown
í Factory configured - do not alter	JP23	unknown

SRAM JUMPER CONFIGURATION						
Size	JP1	JP2	JP3	JP4	JP12	JP13
64KB	pins 2 & 3	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 1 & 2
128KB	pins 1 & 2	pins 2 & 3	pins 1 & 2	pins 2 & 3	pins 1 & 2	pins 2 & 3
256KB	pins 4 & 5	pins 2 & 3	pins 2 & 3	pins 2 & 3	pins 2 & 3	pins 2 & 3

Note: Pins designated should be in the closed position.

SRAM CONFIGURATION			
Size	Cache SRAM	Location	TAG
64KB	(8) 8K x 8	Banks 0 & 1	(2) 16K x 4
128KB	(4) 32K x 8	Bank 0	(2) 16K x 4
256KB	(8) 32K x 8	Banks 0 & 1	(2) 16K x 4

CPU JUMPER CONFIGURATION			
CPU	JP5	JP7	JP15
80486DX2	pins 1 & 2 closed	pins 1 & 2 closed	closed
80486DX	pins 1 & 2 closed	pins 1 & 2 closed	closed
80487SX	pins 2 & 3 closed	pins 1 & 2 closed	closed
80486SX	pins 2 & 3 closed	pins 2 & 3 closed	open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE
128MB	(4) 16M x 9	(4) 16M x 9