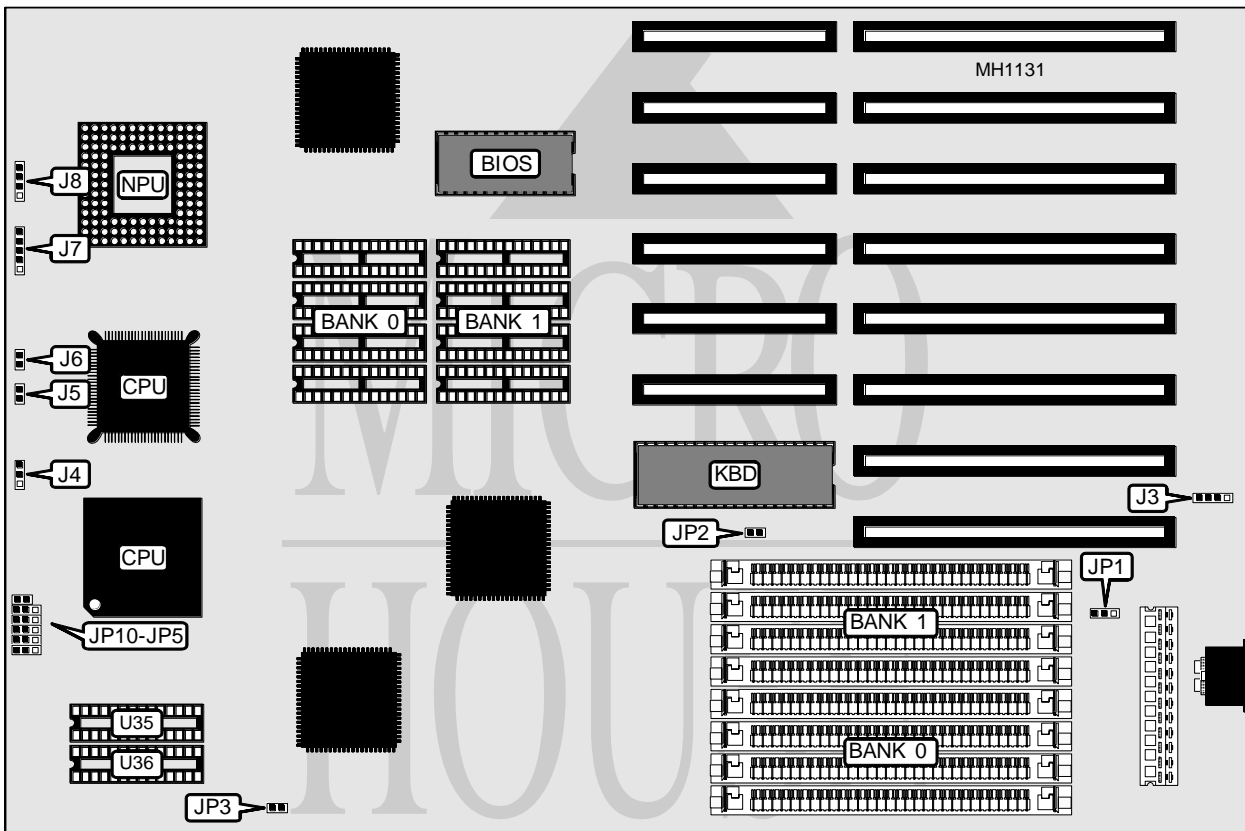


ANIX TECHNOLOGY CORPORATION 386AWP CACHE (DX VER.)

Processor	80386DX
Processor Speed	33/40MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
Cache	32/64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J3	Reset switch	J6
Turbo switch	J4	Power LED & keylock	J7
Turbo LED	J5	Speaker	J8

Continued on next page . . .

ANIX TECHNOLOGY CORPORATION

386AWP CACHE (DX VER.)

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Power good signal detect from power supply	JP1	pins 1 & 2 closed
Power good signal detect from board	JP1	pins 2 & 3 closed
í Monitor type select monochrome	JP2	Open
Monitor type select color	JP2	Closed
í Bus speed select ATCLK/8	JP3	Closed
Bus speed select ATCLK/6	JP3	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG (U35)	TAG (U36)
32KB	(4) 8K x 8	NONE	(1) 8K x 8	NONE
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	NONE
128KB	(4) 32K x 8	NONE	(1) 8K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8	(1) 8K x 8	(1) 8K x 8

CACHE JUMPER CONFIGURATION						
Size	JP5	JP6	JP7	JP8	JP9	JP10
32KB	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3	Open
64KB	1 & 2	1 & 2	2 & 3	2 & 3	1 & 2	Open
128KB	2 & 3	1 & 2	2 & 3	2 & 3	1 & 2	Closed
256KB	2 & 3	2 & 3	2 & 3	2 & 3	1 & 2	Closed

Note: Pins designated should be in the closed position.