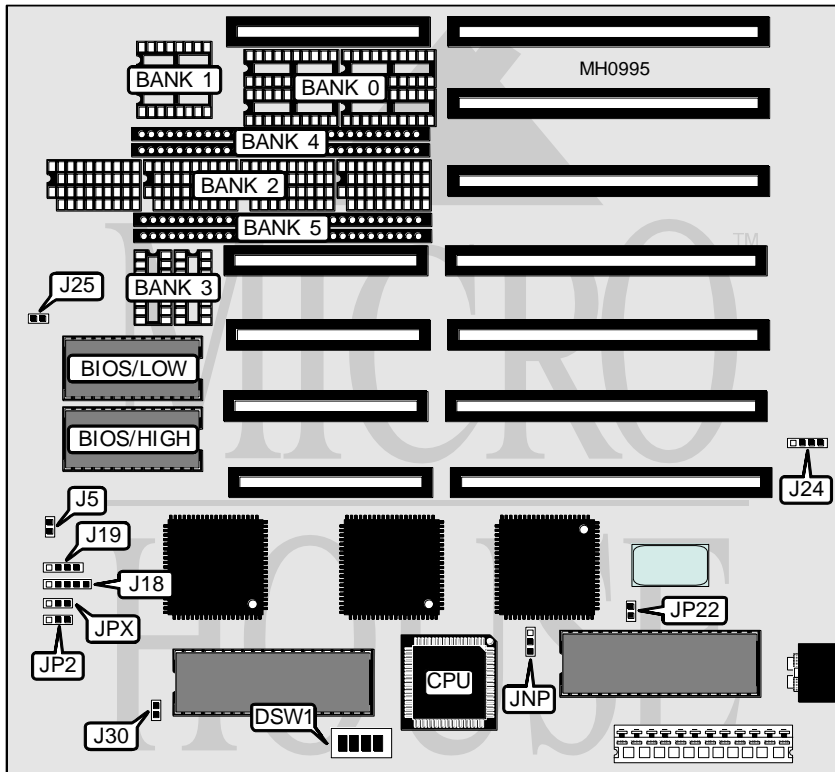


# UNIDENTIFIED

## ELT-286B-120GM

**Processor** 80286  
**Processor Speed** 10/12MHz  
**Chip Set** G-2  
**Max. Onboard DRAM** 4MB  
**Cache** None  
**BIOS** AMI/Phoenix  
**Dimensions** 220mm x 220mm  
**I/O Options** None  
**NPU Options** 80287



### CONNECTIONS

Purpose	Location	Purpose	Location
Turbo LED	J5	External battery	J24
Power LED & keylock	J18	Turbo switch	J25
Speaker	J19	Reset switch	J30

*Continued on next page . . .*

## UNIDENTIFIED

## ELT-286B-120GM

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Factory configured - do not alter	DSW1/1	N/A
í NPU mode select synchronous with CPU	JNP	pins 1 & 2 closed
NPU mode select asynchronous with CPU	JNP	pins 2 & 3 closed
í Wait state select CTRL-ALT-0 = 0 wait, CTRL-ALT-1 is 1 wait	JP2	pins 1 & 2 closed
Wait states select zero	JP2	pins 2 & 3 closed
Wait state select one	JP2	Open
í Factory test disabled	JP22	Open
Factory test enabled	JP22	Closed
í CPU speed select switchable from keyboard	JP25	Open
CPU speed select fast	JP25	Closed
í Factory configured - do not alter	JPX	N/A

DRAM CONFIGURATION						
Size	Bank 0	Bank 1	Bank 2	Bank 3	Bank 4	Bank 5
256KB	(4) 44256	(2) 41256	NONE	NONE	NONE	NONE
256KB	NONE	NONE	NONE	NONE	(2) 256K x 9	NONE
512KB	(4) 44256	(2) 41256	NONE	NONE	NONE	NONE
512KB	NONE	NONE	NONE	NONE	(2) 256K x 9	NONE
640KB	(4) 44256	(2) 41256	(4) 4464	(4) 4164	NONE	NONE
640KB	NONE	NONE	(4) 4464	(4) 4164	(2) 256K x 9	NONE
1MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	NONE	NONE
1MB	NONE	NONE	NONE	NONE	(2) 256K x 9	(2) 256K x 9
2MB	NONE	NONE	NONE	NONE	(2) 1M x 9	NONE
4MB	NONE	NONE	NONE	NONE	(2) 1M x 9	(2) 1M x 9

DRAM SWITCH CONFIGURATION			
Size	DSW1/2	DSW1/3	DSW1/4
256KB	On	On	On
512KB	On	On	Off
640KB	On	Off	On
1MB	On	Off	Off
2MB	Off	Off	On
4MB	Off	Off	Off