SILICON VALLEY COMPUTER, INC.
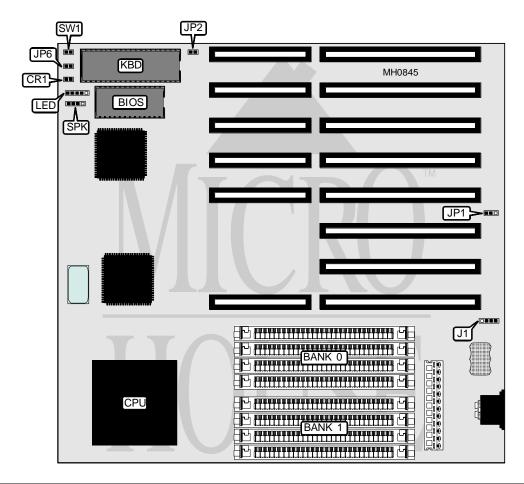# 4 8 6 B B

Processor	80486SX/80486DX
Processor Speed	16/25/33MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
Cache	None
BIOS	SVC
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	None

CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	CR1	Power LED & keylock	LED
External battery	J1	Speaker	SPK
Turbo switch	JP6	Reset switch	SW1

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP1	pins 2 & 3 closed
CMOS memory clear	JP1	pins 1 & 2 closed
í Monitor type select CGA	JP2	Closed
Monitor type select monochrome/EGA/VGA	JP2	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9