GVC TECHNOLOGIES, INC.

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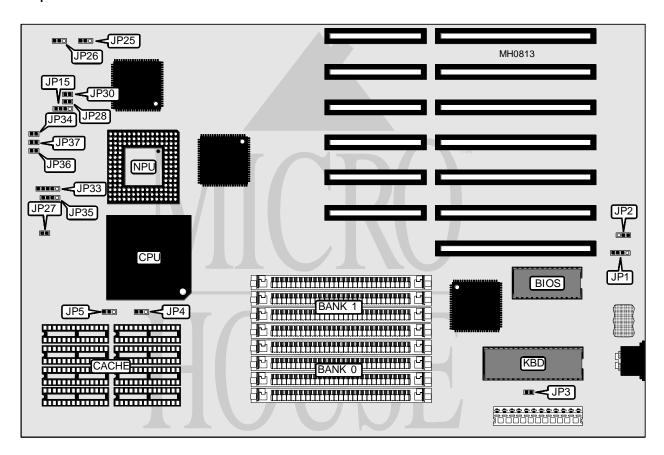
80486SX/80487SX/80486DX **Processor**

Processor Speed 16/20/25/33MHz

Chip Set ETEQ Max. Onboard DRAM 32MB Cache 64/256KB **BIOS** AMI

Dimensions 330mm x 218mm

I/O Options None **NPU Options** 4167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP1	Speaker	JP35
Power LED & keylock	JP33	Turbo LED	JP36
Reset switch	JP34	Turbo switch	JP37

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USER CONFIGURABLE SETTINGS			
Function	Jumper	Position	
í CMOS memory normal operation	JP2	pins 2 & 3 closed	
CMOS memory clear	JP2	pins 1 & 2 closed	
í Monitor type select color	JP3	Closed	
Monitor type select monochrome	JP3	Open	
í Factory configured - do not alter	JP15	N/A	

	DRAM CONFIGURATION	
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION		
Size	Bank 0	
64KB	(8) 8K x 8	
256KB	(8) 32K x 8	

CACHE JUMPER CONFIGURATION					
Size	JP25	JP26	JP27	JP28	JP30
64KB	1 & 2	1 & 2	Closed	Closed	Closed
256KB	2 & 3	2 & 3	Open	Open	Open
Note: Pins designated should be in the closed position.					

	CPU TYPE CONFIGURATION	
Type	JP4	JP5
80486SX	Open	pins 1 & 2 closed
80487SX	pins 1 & 2 closed	pins 2 & 3 closed
80486DX	pins 2 & 3 closed	pins 2 & 3 closed