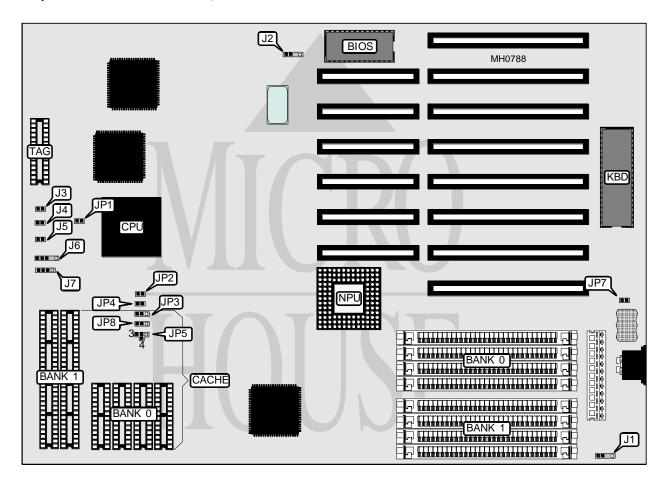
J-BOND COMPUTER SYSTEMS CORPORATION A340C-G02

Processor 80386DX **Processor Speed** 33/40MHz **Chip Set ETEQ** Max. Onboard DRAM 32MB

64/128/256KB Cache **Dimensions** 330mm x 218mm

BIOS AMI I/O Options None **NPU Options** 80387/3167



CONNECTIONS				
Purpose	Location	Purpose	Location	
External battery	J1	Turbo LED	J5	
External battery	J2	Power LED & keylock	J6	
Reset switch	J3	Speaker	J7	
Turbo switch	J4			
Note: Either connector J1 or J2 may be used to connect the external battery.				

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USER CONFIGURABLE SETTINGS			
Function	Jumper	Position	
í CPU speed switchable from turbo switch	JP1	Open	
CPU speed switchable from turbo switch or keyboard	JP1	Closed	
í Battery type select internal	JP7	Closed	
Battery type select external	JP7	Open	

DRAM CONFIGURATION				
Size	Bank 0	Bank 1		
1MB	(4) 256K x 9	NONE		
2MB	(4) 256K x 9	(4) 256K x 9		
4MB	(4) 1M x 9	NONE		
8MB	(4) 1M x 9	(4) 1M x 9		
16MB	(4) 4M x 9	NONE		
32MB	(4) 4M x 9	(4) 4M x 9		

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG	
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	
128KB	(4) 32K x 8	NONE	(1) 8K x 8	
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	

CACHE JUMPER CONFIGURATION					
Size	JP2	JP3	JP4	JP5	JP8
64KB	Open	1 & 2	Open	1 & 2	1 & 2
128KB	Closed	2 & 3	Closed	2 & 3	1 & 2
256KB	Closed	2 & 3	Closed	2 & 4	2 & 3
Note: Pins designated should be in the closed position.					