J-BOND COMPUTER SYSTEMS CORPORATION
J B A 3 2 5 C - D / J B A 3 3 3 C - D

Processor	80386DX
Processor Speed	25/33MHz
Chip Set	C & T
Max. Onboard DRAM	32MB
Cache	None
Dimensions	330mm x 218mm
BIOS	AMI
I/O Options	None
NPU Options	80387/3167
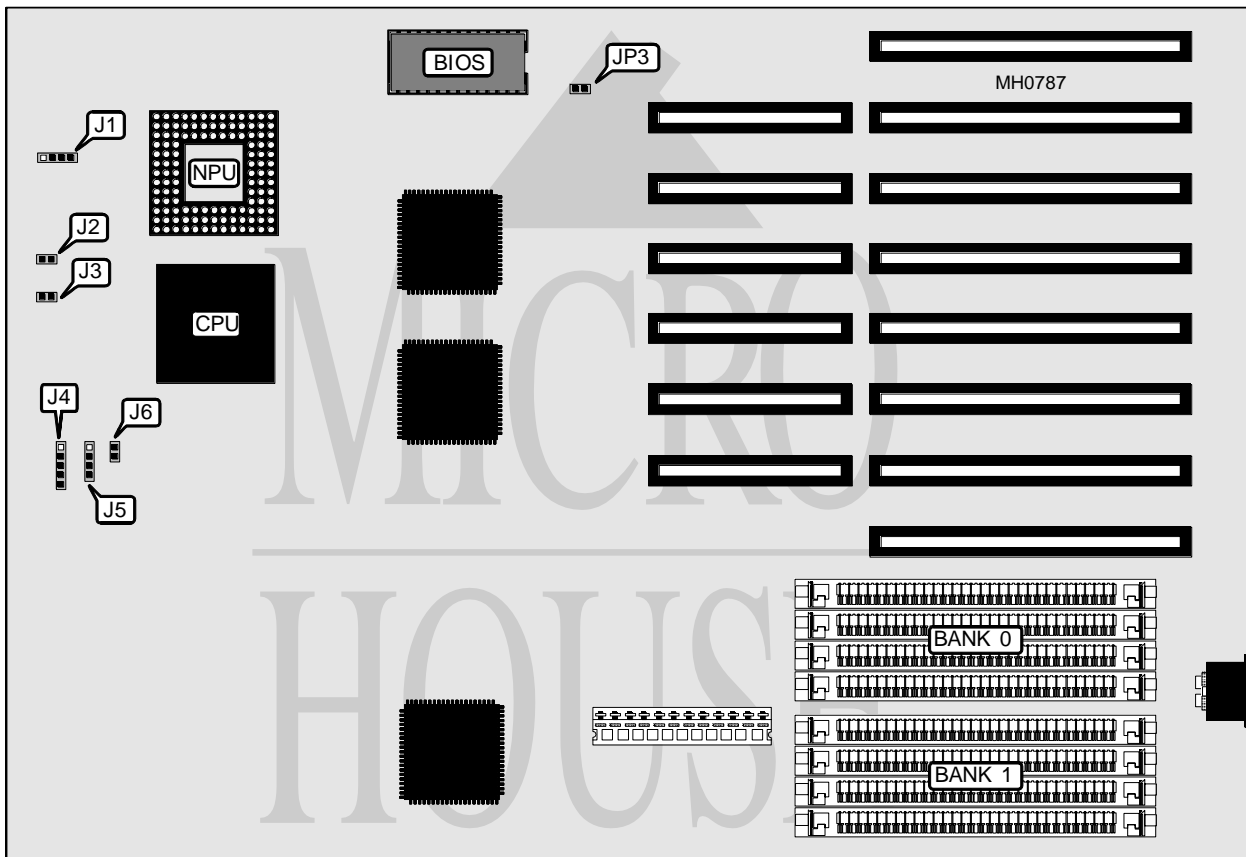
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Power LED & keylock	J4
Turbo LED	J2	Speaker	J5
Turbo switch	J3	Reset switch	J6

Continued on next page . . .

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CPU speed switchable from turbo switch	JP3	Open
CPU speed switchable from turbo switch and keyboard	JP3	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9