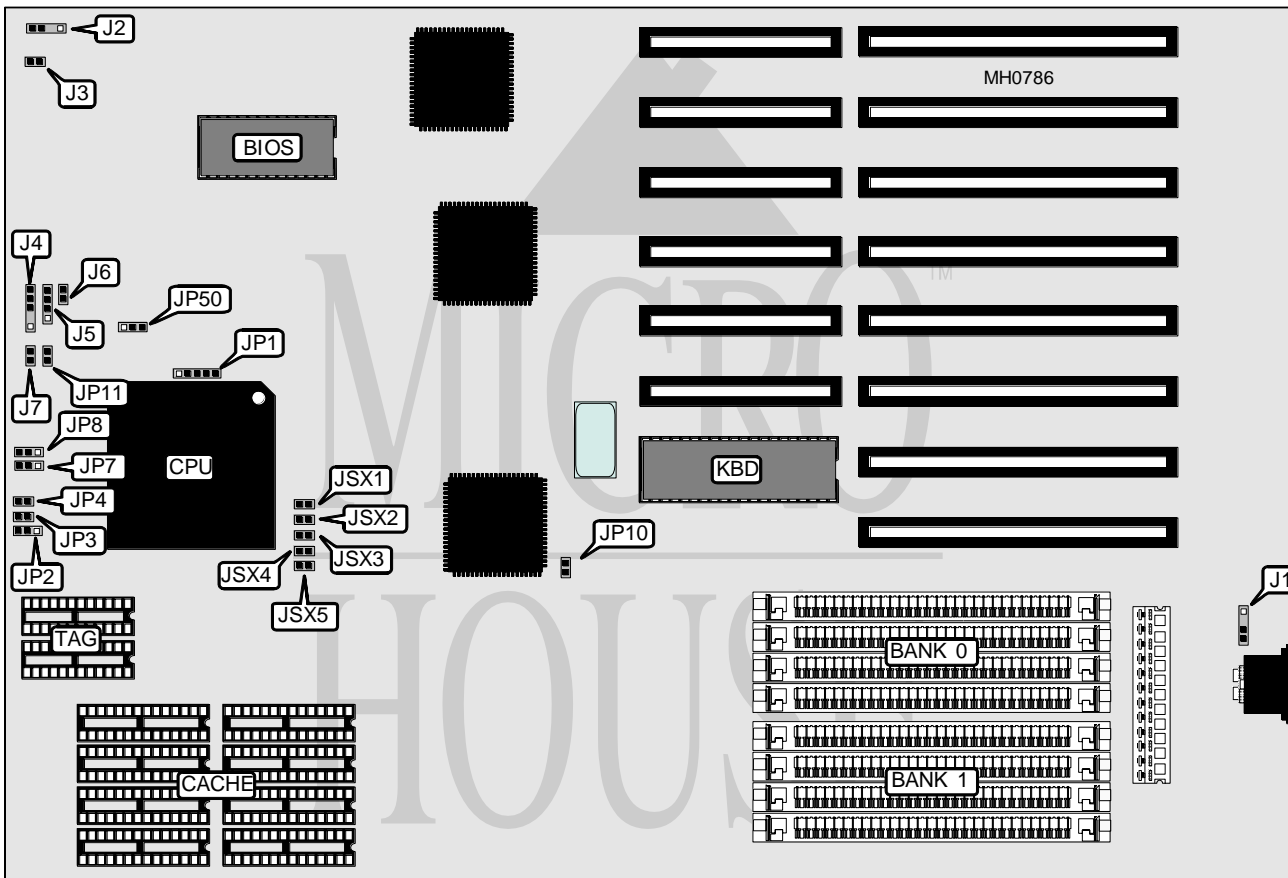


J-BOND COMPUTER SYSTEMS CORPORATION

JB A450CC

Processor	80486SX/80487SX/80486DX
Processor Speed	20/25/33/50MHz
Chip Set	ETEQ
Max. Onboard DRAM	32MB
Cache	64/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	4167 (on JWT-467 piggy-back card)



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Speaker	J5
External battery	J2	Reset switch	J6
Turbo LED	J3	Turbo switch	J7
Power LED & keylock	J4	JWT-467 piggy-back card	JP1
Note: Either connector J1 or J2 may be used to connect the external battery.			

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J-BOND COMPUTER SYSTEMS CORPORATION

JB A450CC

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
CPU speed switchable from turbo switch	JP11	Open
CPU speed switchable from turbo switch or keyboard	JP11	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION		
Size	Bank 0	TAG
64KB	(8) 8K x 8	(2) 16K x 4
256KB	(8) 32K x 8	(2) 16K x 4

CACHE JUMPER CONFIGURATION					
Size	JP2	JP3	JP4	JP7	JP8
64KB	2 & 3	Open	Open	2 & 3	2 & 3
256KB	1 & 2	Closed	Closed	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION							
Type	JP10	JP50	JSX1	JSX2	JSX3	JSX4	JSX5
80486SX	Closed	2 & 3	Closed	Open	Closed	Open	Open
80487SX	Closed	2 & 3	Open	Open	Open	Closed	Closed
80486DX-33MHz	Open	2 & 3	Open	Closed	Closed	Open	Closed
80486DX-50MHz	Open	1 & 2	Open	Closed	Closed	Open	Closed