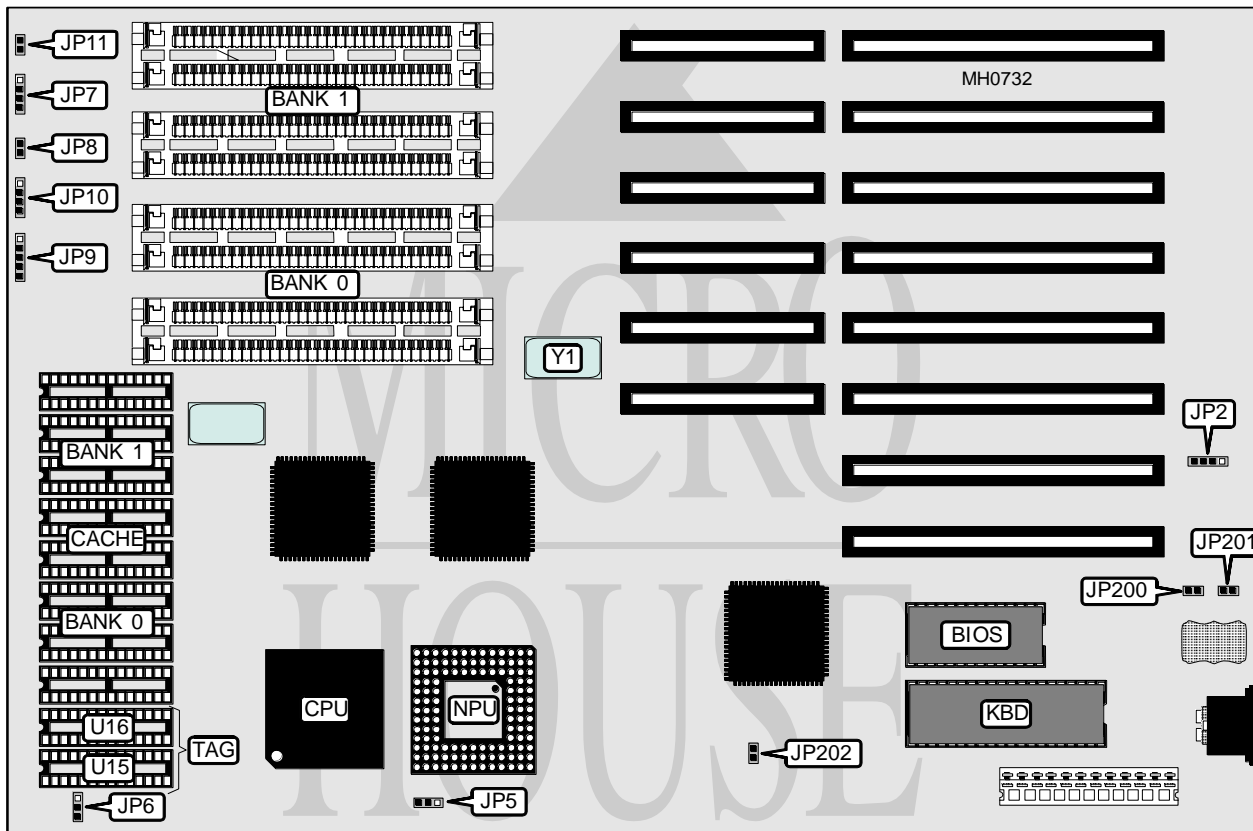


# CORE PACIFIC ELECTRONICS, INC

## 386 MAINBOARD

**Processor** 80386DX  
**Processor Speed** 33/40MHz  
**Chip Set** Forex  
**Max. Onboard DRAM** 32MB  
**Cache** 32/64/128KB  
**BIOS** AMI  
**Dimensions** 330mm x 218mm  
**I/O Options** None  
**NPU Options** 80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP2	Power LED & keylock	JP9
Turbo LED	JP7	Speaker	JP10
Reset switch	JP8	Turbo switch	JP11

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# CORE PACIFIC ELECTRONICS, INC

## 386 MAINBOARD

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í NPU mode select synchronous with CPU	JP5	pins 1 & 2 closed
NPU mode select synchronous with oscillator installed at Y1	JP5	pins 2 & 3 closed
í CPU speed select forced slow	JP11	Closed
CPU speed select switchable at keyboard	JP11	Open
í External battery select 3.6VDC	JP200	Closed
External battery select 6.0VDC	JP200	Open
í Battery type select internal	JP201	Closed
Battery type select external	JP201	Open
í CMOS memory normal operation	JP202	Open
CMOS memory clear	JP202	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1 M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	U15	U16
32KB	(4) 8K x 8	NONE	NONE	(1) 8K x 8
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	(1) 8K x 8
128KB	NONE	(4) 32K x 8	(1) 32K x 8	NONE

CACHE JUMPER CONFIGURATION	
Size	JP6
32KB	pins 1 & 2 closed
64KB	pins 2 & 3 closed
128KB	pins 2 & 3 closed