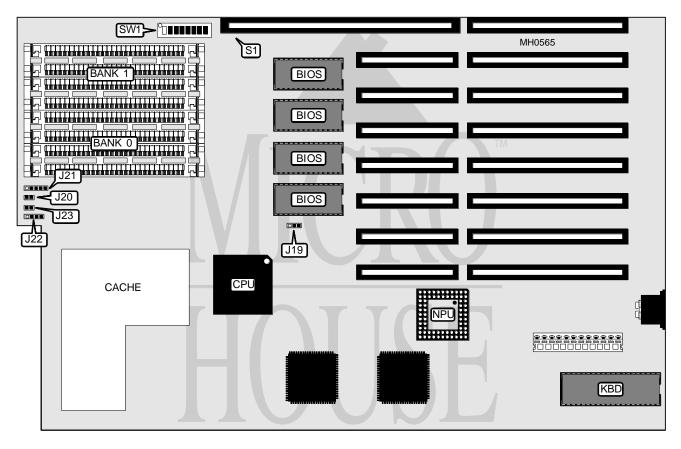
CORE INTERNATIONAL, INC. **ATOMIZER 386/20**

80386DX **Processor Processor Speed** 20MHz **Chip Set** NEC Max. Onboard DRAM 8MB

Cache Unidentified **BIOS** Award/Phoenix **Dimensions** 330mm x 218mm

I/O Options 32-bit external memory card slot

NPU Options 80387



CONNECTIONS					
Purpose	Location	Purpose	Location		
Reset switch	J20	Turbo LED	J23		
Power LED & keylock	J21	32-bit external memory card	S1		
Speaker	J22				

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USER CONFIGURABLE SETTINGS				
Function	Jumper/Switch	Position		
í BIOS type select 27256	J19	pins 1 & 2 closed		
BIOS type select 27128	J19	pins 2 & 3 closed		
í Conventional memory size select 512KB	SW1/1	On		
Conventional memory size select 640KB	SW1/1	Off		
í Onboard EGA BIOS enabled	SW1/5	On		
Onboard EGA BIOS disabled	SW1/5	Off		
í Factory configured - do not alter	SW1/6	Off		
í CPU speed at boot select 20MHz	SW1/7	On		
CPU speed at boot select 8MHz	SW1/7	Off		
í Monitor type select monochrome	SW1/8	Off		
Monitor type select color	SW1/8	On		
Note: The location of J19 is unidentified.				

DRAM CONFIGURATION					
Size	Bank 0	Bank 1			
1MB	(4) 256K x 9	NONE			
2MB	(4) 256K x 9	(4) 256K x 9			
4MB	(4) 1M x 9	NONE			
8MB	(4) 1M x 9	(4) 1M x 9			

DRAM SWITCH CONFIGURATION					
Size	SW1/2	SW1/3	SW1/4		
1MB	Off	On	On		
2MB	Off	Off	On		
4MB	Off	On	Off		
8MB	On	Off	Off		

CACHE CONFIGURATION

Although cache chips are diagrammed in the users manual, no information concerning cache sizes or configurations are listed in the users manual nor are they available from the manufacturer.