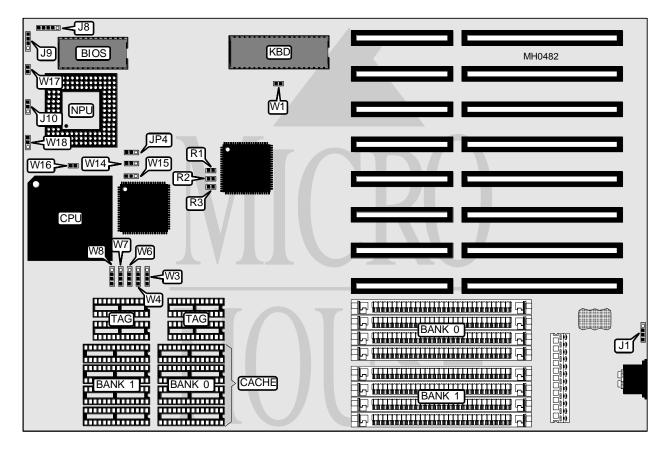
GENESYS ATE, INC. G X 4861

Processor	80486SX/80487SX/80486DX		
Processor Speed	20/33/50MHz		
Chip Set	UMC		
Max. Onboard DRAM	32MB		
Cache	64/128/256KB		
BIOS	AMI/Award		
Dimensions	330mm x 218mm		
I/O Options	None		
NPU Options	4167		



CONNECTIONS				
Purpose Location Purpose Location				
External battery	J1	Turbo switch	J10	
Power LED & keylock	J8	Reset switch	W17	
Speaker	J9	Turbo LED	W18	

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USER CONFIGURABLE SETTINGS			
Function	Jumper	Position	
í Monitor type select monochrome	W1	Open	
Monitor type select color	W1	Closed	

DRAM CONFIGURATION				
Size	Bank 0	Bank 1		
1MB	(4) 256K x 9	NONE		
2MB	(4) 256K x 9	(4) 256K x 9		
4MB	(4) 1M x 9	NONE		
5MB	(4) 256K x 9	(4) 1M x 9		
8MB	(4) 1M x 9	(4) 1M x 9		
16MB	(4) 4M x 9	NONE		
17MB	(4) 256K x 9	(4) 4M x 9		
20MB	(4) 1M x 9	(4) 4M x 9		
32MB	(4) 4M x 9	(4) 4M x 9		

CACHE CONFIGURATION					
Size Bank 0 Bank 1 TAG					
64KB	(4) 8K x 8	(4) 8K x 8	(4) 16K x 4		
128KB (4) 32K x 8		NONE	(4) 16K x 4		
256KB	(4) 32K x 8	(4) 32K x 8	(4) 16K x 4		

CACHE JUMPER CONFIGURATION					
Size	W3	W4	W6	W7	W8
64KB	1&2	1&2	1&2	1&2	3 & 4
128KB	1 & 2	1&2	3 & 4	3 & 4	3 & 4
256KB	3 & 4	3 & 4	3 & 4	1 & 2	3 & 4
Note: Pins designated should be in the closed position.					

CPU TYPE CONFIGURATION					
Type W2 W14 W15 W16					
80486SX	Closed	Open	pins 2 & 3 closed	Open	
80487SX	Open	pins 2 & 3 closed	pins 1 & 2 closed	Open	
80486DX	Open	pins 1 & 2 closed	pins 1 & 2 closed	Closed	

CPU SPEED CONFIGURATION					
Speed	JP4	R1	R2	R3	
20MHz	pins 2 & 3 closed	Open	Open	Closed	
25MHz	pins 1 & 2 closed	Open	Open	Open	
33MHz	pins 1 & 2 closed	Closed	Open	Open	
40MHz	pins 1 & 2 closed	Open	Closed	Open	
50MHz	pins 1 & 2 closed	Closed	Closed	Open	