FIRST INTERNATIONAL COMPUTER, INC.
386SX-16/20V

Processor	80386SX
Processor Speed	16/20MHz
Chip Set	Symphony
Max. Onboard DRAM	8MB
Cache	None
BIOS	AMI/Award/Phoenix
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80387SX
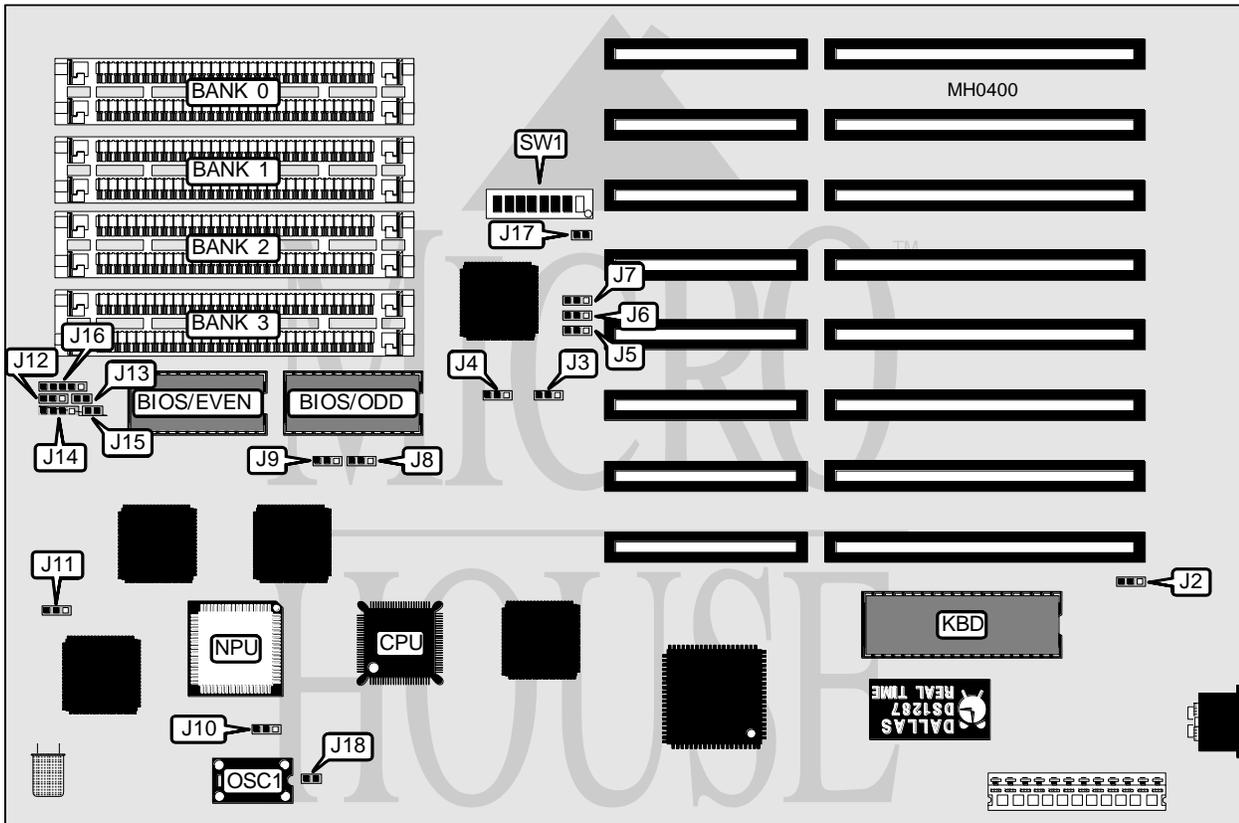
CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	J13	Reset switch	J15
Speaker	J14	Power LED & keylock	J16

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Monitor type select monochrome	J2	pins 2 & 3 closed
Monitor type select color	J2	pins 1 & 2 closed
640K to 1M DRAM re-mapping disabled	J5	pins 2 & 3 closed
Memory access 2 wait states/ I/O select 6 wait states	J8	pins 1 & 2 closed
Memory access 1 wait state/ I/O select 4 wait states	J8	pins 2 & 3 closed
NPU enabled	J9	pins 1 & 2 closed
NPU disabled	J9	pins 2 & 3 closed
í NPU mode select synchronous with CPU clock	J10	pins 1 & 2 closed
NPU mode select synchronous with crystal installed at OSC1	J10	pins 2 & 3 closed
CPU speed select 20MHz	J11	pins 1 & 2 closed
CPU speed select 16MHz	J11	pins 2 & 3 closed
í CPU speed select fast	J12	Open
CPU speed select slow	J12	pins 2 & 3 closed
CPU speed select switchable from keyboard	J12	pins 1 & 2 closed
í Factory configured - do not alter	J17	N/A
í Factory configured - do not alter	J18	N/A
í Shadow RAM enabled	SW1/1	On
Shadow RAM disabled	SW1/1	Off
í Factory configured - do not alter	SW1/2	N/A

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(2) 256K x 9	NONE	NONE	NONE
1MB	(2) 256K x 9	(2) 256K x 9	NONE	NONE
1.5MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	NONE
2MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE	NONE	NONE
3MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9	NONE	NONE
5MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	NONE
8MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9

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DRAM JUMPER/SWITCH CONFIGURATION						
Size	J6	J7	SW1/5	SW1/6	SW1/7	SW1/8
512KB	2 & 3	2 & 3	Off	Off	Off	Off
1MB	2 & 3	2 & 3	On	Off	Off	Off
1.5MB	2 & 3	2 & 3	Off	On	Off	Off
2MB	2 & 3	2 & 3	On	On	Off	Off
2MB	1 & 2	1 & 2	Off	Off	On	On
3MB	2 & 3	1 & 2	Off	On	On	Off
4MB	1 & 2	1 & 2	On	Off	On	On
5MB	2 & 3	1 & 2	On	On	On	Off
6MB	1 & 2	1 & 2	Off	On	On	On
8MB	1 & 2	1 & 2	On	On	On	On
Note: Pins designated should be in the closed position.						

DRAM WAIT STATE/WRITE SELECT CONFIGURATION			
Wait state	Wait select	SW1/3	SW1/4
2	2	On	On
2	1	On	Off
1	1	Off	Off

BIOS CONFIGURATION		
Size	J3	J4
27128	pins 1 & 2 closed	pins 1 & 2 closed
27256	pins 1 & 2 closed	pins 2 & 3 closed
27512	pins 2 & 3 closed	pins 2 & 3 closed