

FIRST INTERNATIONAL COMPUTER, INC.**386-20/25V**

Processor	80386DX
Processor Speed	20/25MHz
Chip Set	Symphony
Max. Onboard DRAM	8MB
Cache	None
BIOS	Award/Phoenix
Dimensions	330mm x 218mm
I/O Options	32-bit external memory card slot
NPU Options	80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
Reset switch	J12	Power LED & keylock	J15
Speaker	J13	32-bit external memory card	S1
Turbo LED	J14		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	J3	Open
í Factory configured - do not alter	J4	Open
í Memory select 2 wait states/ I/O select 6 wait states	J5	Closed
Memory select 1 wait state/ I/O select 4 wait states	J5	Open
BIOS type select 27256	J6	pins 1 & 2 closed
BIOS type select 27512	J6	pins 2 & 3 closed
CPU speed select 20MHz	J9	Open
CPU speed select 25MHz	J9	Closed
í CPU speed select switchable from keyboard	J15	pins 2 & 3 closed
CPU speed select fast	J15	pins 1 & 2 closed
CPU speed select slow	J15	Open
Bus speed select iOSC/2	J17	pins 1 & 2 closed
Bus speed select 8MHz	J17	pins 2 & 3 closed
í Shadow RAM enabled	SW1/switch 6	closed
Shadow RAM disabled	SW1/switch 6	open
	SW1/switch 7	closed
DRAM read select 2 wait states/write select 2 wait states	SW1/switch 8	closed
	SW1/switch 7	closed
DRAM read select 2 wait states/write select 1 wait state	SW1/switch 8	open
	SW1/switch 7	open
DRAM read select 1 wait state/write select 1 wait state	SW1/switch 8	open
NPU enabled	SW1/switch 9	closed
NPU disabled	SW1/switch 9	open
í Monitor type select monochrome	SW1/switch 10	open
Monitor type select color	SW1/switch 10	closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
3MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	NONE
4MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE	NONE	NONE
6MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
10MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9

Note: Banks 2 & 3 are utilized when a 32-bit external memory card is installed at S1.

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DRAM JUMPER/SWITCH CONFIGURATION						
Size	J7	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5
1MB	2 & 3	Off	Off	Off	Off	Off
2MB	2 & 3	On	Off	Off	Off	Off
3MB	2 & 3	Off	On	Off	Off	Off
4MB	2 & 3	On	On	Off	Off	Off
4MB	1 & 2	Off	Off	On	On	On
6MB	2 & 3	Off	On	On	Off	Off
8MB	1 & 2	On	Off	On	On	On
10MB	2 & 3	On	On	On	Off	Off
12MB	1 & 2	Off	On	On	On	On
16MB	1 & 2	On	On	On	On	On

Note: Pins designated should be in the closed position.

DRAM READ/WRITE CONFIGURATION			
Wait state	Read state	SW1/7	SW1/8
2	2	On	On
2	1	On	Off
1	1	Off	Off