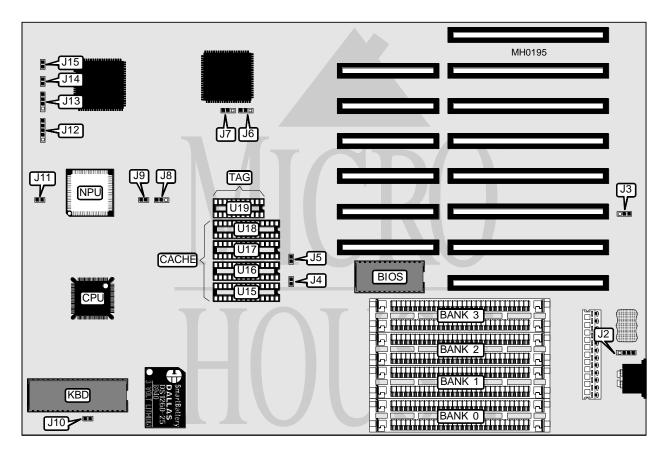
ORCHID TECHNOLOGY PRIVILEGE 386SX

Processor 80386SX **Processor Speed** 20/25MHz **Chip Set** OPTI Max. Onboard DRAM 16MB Cache 32/64KB **BIOS** AMI

330mm x 218mm **Dimensions**

I/O Options None **NPU Options** 80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J2	Speaker	J13
Turbo switch	J9	Turbo LED	J14
Power LED & keylock	J12	Reset switch	J15

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USER CONFIGURABLE SETTINGS			
Function	Jumper	Position	
í Factory configured - do not alter	J3	pins 2 & 3 closed	
í Factory configured - do not alter	J8	pins 1 & 2 closed	
í Factory configured - do not alter	J10	Closed	
í NPU mode select synchronous with CPU	J11	Open	
NPU mode select asynchronous with CPU	J11	Closed	

		DRAM CONFIGURATI	ON	
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(2) 256K x 9	(2) 256K x 9	NONE	NONE
2MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9
4MB	(2) 1M x 9	(2) 1M x 9	NONE	NONE
5MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	NONE
8MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9
10MB	(2) 1M x 9	(2) 4M x 9	NONE	NONE
16MB	(2) 4M x 9	(2) 4M x 9	NONE	NONE

CACHE CONFIGURATION			
Size	Cache	Location	TAG (U19)
32KB	(4) 8K x 8	U15 - U18	(1) 8K x 8
64KB	(2) 32K x 8	U15 & U16	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	J4	J5	J6	J7
32KB	Open	Closed	pins 1 & 2 closed	pins 2 & 3 closed
64KB	Closed	Open	pins 2 & 3 closed	pins 2 & 3 closed