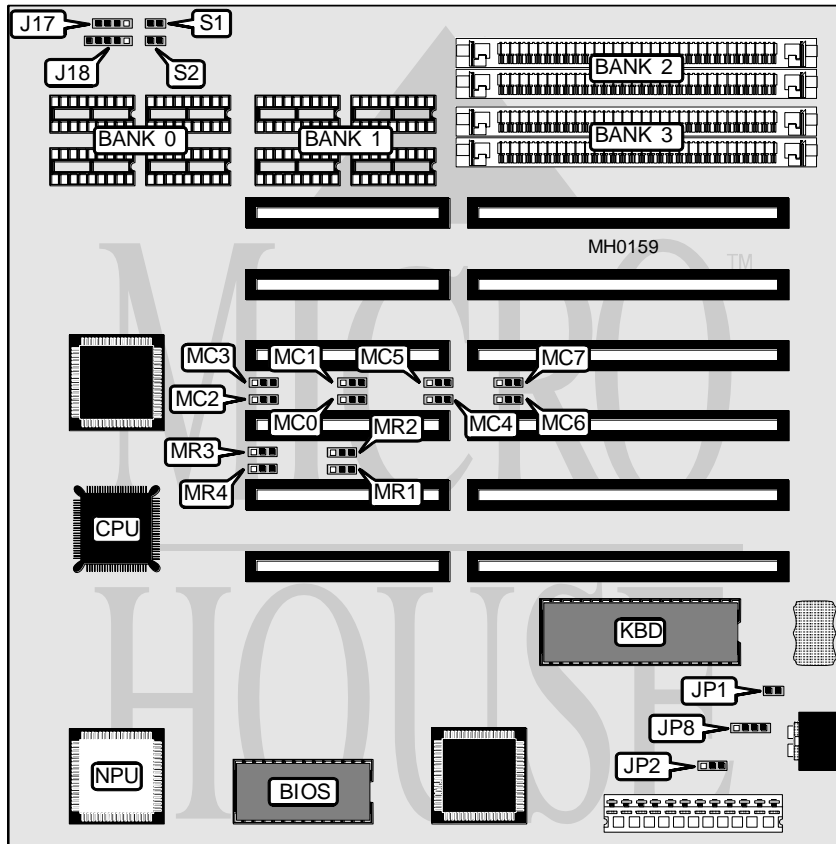


# SILICON VALLEY COMPUTER, INC. MINI 386SX

**Processor** 80386SX  
**Processor Speed** 16/25/33MHz  
**Chip Set** C & T  
**Max. Onboard DRAM** 16MB  
**Cache** None  
**BIOS** C & T  
**Dimensions** 220mm x 220mm  
**I/O Options** None  
**NPU Options** 80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J17	Reset switch	S1
Power LED & keylock	J18	Turbo switch	S2
External battery	JP8		

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# SILICON VALLEY COMPUTER, INC.

## MINI 386SX

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select CGA	JP1	Closed
Monitor type select monochrome, EGA, or VGA	JP1	Open
í CMOS memory normal operation	JP2	pins 2 & 3 closed
CMOS memory clear	JP2	pins 1 & 2 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB <sup>1</sup>	(4) 44256	(4) 44256	NONE	NONE
1MB <sup>2</sup>	NONE	NONE	(2) 256K x 9	(2) 256K x 9
2MB <sup>1</sup>	(4) 44256	(4) 44256	(2) 256K x 9	(2) 256K x 9
2MB <sup>1</sup>	(4) 441000	NONE	NONE	NONE
2MB <sup>2</sup>	(4) 44256	(4) 44256	(2) 256K x 9	(2) 256K x 9
2MB <sup>2</sup>	NONE	NONE	(2) 1M x 9	NONE
3MB <sup>1</sup>	(4) 44256	(4) 44256	(2) 1M x 9	NONE
3MB <sup>2</sup>	(4) 441000	NONE	(2) 256K x 9	(2) 256K x 9
4MB <sup>1</sup>	(4) 441000	(4) 441000	NONE	NONE
4MB <sup>2</sup>	NONE	NONE	(2) 1M x 9	(2) 1M x 9
5MB <sup>1</sup>	(4) 44256	(4) 44256	(2) 1M x 9	(2) 1M x 9
6MB <sup>1</sup>	(4) 441000	(4) 441000	(2) 1M x 9	NONE
6MB <sup>2</sup>	(4) 441000	NONE	(2) 1M x 9	(2) 1M x 9
8MB <sup>1</sup>	(4) 441000	(4) 441000	(2) 1M x 9	(2) 1M x 9
8MB <sup>2</sup>	(4) 441000	(4) 441000	(2) 1M x 9	(2) 1M x 9
8MB <sup>2</sup>	NONE	NONE	(2) 4M x 9	NONE
9MB <sup>1</sup>	(4) 44256	(4) 44256	(2) 4M x 9	NONE
10MB <sup>2</sup>	NONE	NONE	(2) 4M x 9	(2) 1M x 9
12MB <sup>1</sup>	(4) 441000	(4) 441000	(2) 4M x 9	NONE
16MB <sup>2</sup>	NONE	NONE	(2) 4M x 9	(2) 4M x 9

Note <sup>1</sup>: On jumpers MR1 through MR4 and jumpers MC0 through MC7, pins 1 & 2 are closed.  
 Note <sup>2</sup>: On jumpers MR1 through MR4 and jumpers MC0 through MC7, pins 2 & 3 are closed.