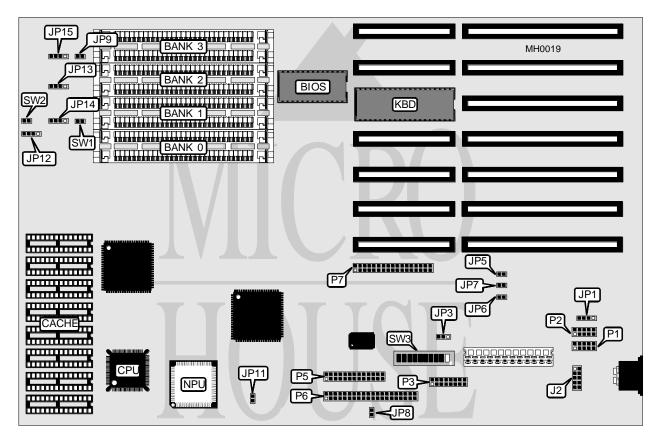
## CACHE COMPUTERS, INC. 3865X-20/25 PLUS (MODEL 320)

Processor
Processor Speed
Chip Set
Max. Onboard DRAM
Cache
BIOS
Dimensions
I/O Options
NPU Options

80386SX 20/25MHz OPTI 16MB 64KB AMI/Cache/Phoenix 330mm x 218mm Parallel port, serial ports (2), game port, floppy drive interface, IDE interface 80387SX



CONNECTIONS			
Purpose	Location	Location	
Auxiliary keyboard	J2 COM2 at 2F8-2FFh		P2
External battery	JP1 Game port at 201h		Р3
IDE LED	JP8	Parallel port at 378-37Fh	P5
Turbo LED	JP12	IDE interface	P6
Speaker	JP13	Floppy drive interface	P7
Power LED & keylock	JP14	Reset switch	SW1
COM1 at 3F8-3FFh	P1	Turbo switch	SW2

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## CACHE COMPUTERS, INC. 3865X-20/25 PLUS (MODEL 320)

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USER CONFIGURABLE SETTINGS			
Function	Jumper/Switch	Position	
í Parallel port enabled	JP3	pins 1 & 2 closed	
Parallel port disabled	JP3	pins 2 & 3 closed	
í IRQ 7 enabled	JP5	Closed	
IRQ 7 disabled	JP5	Open	
í IRQ 3 enabled	JP6	Closed	
IRQ 3 disabled	JP6	Open	
í IRQ 4 enabled	JP7	Closed	
IRQ 4 disabled	JP7	Open	
í Monitor type set color (AMI BIOS)	JP9	Closed	
Monitor type set monochrome (AMI BIOS)	JP9	Open	
í Security enabled (MR BIOS)	JP9	Closed	
Security disabled (MR BIOS)	JP9	Open	
í CMOS memory normal operation	JP11	Open	
CMOS memory clear	JP11	Closed	
í CPU speed switchable from keyboard	JP15	pins 1 & 2 closed	
CPU speed switchable from turbo switch	JP15	pins 2 & 3 closed	
í IDE interface enabled	SW3/1	Off	
IDE interface disabled	SW3/1	On	
í Floppy interface enabled	SW3/2	Off	
Floppy interface disabled	SW3/2	On	
í Factory configured - do not alter	SW3/3	N/A	
í Floppy interface select primary (only with 2 controllers)	SW3/9	On	
Floppy interface select secondary (only with 2 controllers)	SW3/9	Off	
í Configuration select manual	SW3/10	On	
Configuration select automatic	SW3/10	Off	

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## CACHE COMPUTERS, INC. 3865X-20/25 PLUS (MODEL 320)

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DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(2) 256K x 9	(2) 256K x 9	NONE	NONE
2MB	(2) 256K x 9			
2MB	(2) 1M x 9	NONE	NONE	NONE
3MB	(2) 256K x 9	(2) 1M x 9	NONE	NONE
4MB	(2) 1M x 9	(2) 1M x 9	NONE	NONE
5MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	NONE
8MB	(2) 1M x 9			
9MB	(2) 256K x 9	(2) 256K x 9	(2) 4M x 9	NONE
10MB	(2) 1M x 9	(2) 4M x 9	NONE	NONE
12MB	(2) 1M x 9	(2) 1M x 9	(2) 4M x 9	NONE
16MB	(2) 4M x 9	(2) 4M x 9	NONE	NONE

CACHE CONFIGURATION			
Size	Bank 0		
64КВ	(8) 8K x 8		
Note: The location of the cache is unidentified.			

PARALLEL PORT CONFIGURATION			
Setting	SW3/7	SW3/8	
í LPT2	Off	Off	
LPT1	Off	On	
LPT3	On	Off	
Disabled	On	On	

SERIAL PORT CONFIGURATION				
Serial port (P1)	Serial port (P2)	SW3/4	SW3/5	SW3/6
COM1	COM2	Off	Off	Off
Disabled	COM2	Off	Off	On
COM1	Disabled	Off	On	Off
Disabled	Disabled	Off	On	On
COM2	COM1	On	Off	Off
Disabled	COM1	On	Off	On
COM2	Disabled	On	On	Off
Disabled	Disabled	On	On	On