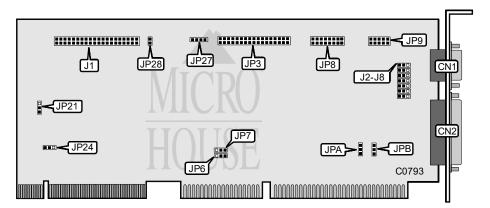
Q3/95

UNIDENTIFIED VL100

Data bus: 32-bit VL-Bus

Size: Three/quarter-length, full-height card Hard drive supported: Two IDE(AT) interface devices

Floppy drives supported: Two 360KB, 720KB, 1.2MB, 1.44MB or 2.88MB drives



| CONNECTIONS | | |
|---------------------------------------|----------|--|
| Function | Location | |
| 9-pin serial port 1 - external | CN1 | |
| 25-pin parallel port - external | CN2 | |
| 40-pin IDE(AT) connector | J1 | |
| 34-pin cable connector - floppy drive | JP3 | |
| 16-pin game port | JP8 | |
| 10-pin serial port 2 - internal | JP9 | |
| 4-pin connector - drive active LED | JP27 | |

| USER CONFIGURABLE SETTINGS | | | |
|-----------------------------------|-----------------|---------|--|
| Function | Location | Setting | |
| í Floppy drive enabled | J6/pins 1 & 2 | closed | |
| Floppy drive disabled | J6/pins 2 & 3 | closed | |
| VL100 supports two floppy drives | J7/pins 1 & 2 | closed | |
| Reserved for factory use | J7/pins 2 & 3 | closed | |
| í IDE interface enabled | JP21/pins 1 & 2 | closed | |
| IDE interface disabled | JP21/pins 2 & 3 | closed | |
| í VL100 supports two IDE drives | JP28 | open | |
| VL100 supports one IDE drive | JP28 | closed | |
| í IDE address is 034h, 038h, 03Ch | JP24/pins 1 & 2 | closed | |
| IDE address is 0B4h, 0B8h, 0BCh | JP24/pins 2 & 3 | closed | |

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| | SERIAL PORTS CONFIGURATION | | | | | |
|----------|----------------------------|-------|-------|-------|-------|-------|
| Port 1 | Port 2 | J2 | J5 | J8 | JP6 | JP7 |
| í COM1 | COM2 | 1 & 2 | 1 & 2 | 1 & 2 | 1 & 2 | 1 & 2 |
| COM2 | COM1 | 1 & 2 | 1 & 2 | 2 & 3 | 1 & 2 | 1 & 2 |
| COM1 | Disabled | 2 & 3 | 1 & 2 | 1 & 2 | 1 & 2 | 1 & 2 |
| Disabled | COM1 | 1 & 2 | 2 & 3 | 2 & 3 | 1 & 2 | 1 & 2 |
| COM2 | Disabled | 2 & 3 | 1 & 2 | 2 & 3 | 1 & 2 | 1 & 2 |
| Disabled | COM2 | 1 & 2 | 2 & 3 | 1 & 2 | 1 & 2 | 1 & 2 |
| COM3 | COM4 | 1 & 2 | 1 & 2 | 1 & 2 | 2 & 3 | 1 & 2 |
| COM4 | COM3 | 1 & 2 | 1 & 2 | 2 & 3 | 2 & 3 | 1 & 2 |
| COM3 | Disabled | 2 & 3 | 1 & 2 | 1 & 2 | 2 & 3 | 1 & 2 |
| Disabled | COM3 | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 | 1 & 2 |
| COM4 | Disabled | 2 & 3 | 1 & 2 | 2 & 3 | 2 & 3 | 1 & 2 |
| Disabled | COM4 | 1 & 2 | 2 & 3 | 1 & 2 | 2 & 3 | 1 & 2 |
| COM1 | COM4 | 1 & 2 | 1 & 2 | 1 & 2 | 1 & 2 | 2 & 3 |
| COM4 | COM1 | 1 & 2 | 1 & 2 | 2 & 3 | 1 & 2 | 2 & 3 |
| COM1 | Disabled | 2 & 3 | 1 & 2 | 1 & 2 | 1 & 2 | 2 & 3 |
| Disabled | COM1 | 1 & 2 | 2 & 3 | 2 & 3 | 1 & 2 | 2 & 3 |
| COM4 | Disabled | 2 & 3 | 1 & 2 | 2 & 3 | 1 & 2 | 2 & 3 |
| Disabled | COM4 | 1 & 2 | 2 & 3 | 1 & 2 | 1 & 2 | 2 & 3 |
| COM2 | COM3 | 1 & 2 | 1 & 2 | 1 & 2 | 2 & 3 | 2 & 3 |
| COM3 | COM2 | 1 & 2 | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 |
| COM2 | Disabled | 2 & 3 | 1 & 2 | 1 & 2 | 2 & 3 | 2 & 3 |
| Disabled | COM2 | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 |
| COM3 | Disabled | 2 & 3 | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 |
| Disabled | COM3 | 1 & 2 | 2 & 3 | 1 & 2 | 2 & 3 | 2 & 3 |
| Disabled | Disabled | 2 & 3 | 2 & 3 | N/A | N/A | N/A |

Note: Designated pins are in the closed position.
The settings of COM3, COM4/ COM1, COM4/ COM2, COM3/ can be implemented only on

card with M5105A4E I/O chip.

| PARALLEL PORT CONFIGURATION | | | | |
|-----------------------------|-------------------|-------------------|--|--|
| LPT | J3 | J4 | | |
| LPT1(378h) | pins 1 & 2 closed | pins 1 & 2 closed | | |
| LPT2(278h) | pins 1 & 2 closed | pins 2 & 3 closed | | |
| LPT3(3BCh) | pins 2 & 3 closed | pins 1 & 2 closed | | |
| Disabled | pins 2 & 3 closed | pins 2 & 3 closed | | |

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| BIOS ADDRESS | | | |
|--------------|--------|--------|--|
| Address | JPA | JPB | |
| íC8000h | closed | closed | |
| D0000h | open | closed | |
| D8000h | closed | open | |
| E0000h | open | open | |