



## PC/AT-COMPATIBLE DATA BUFFER

## FEATURES

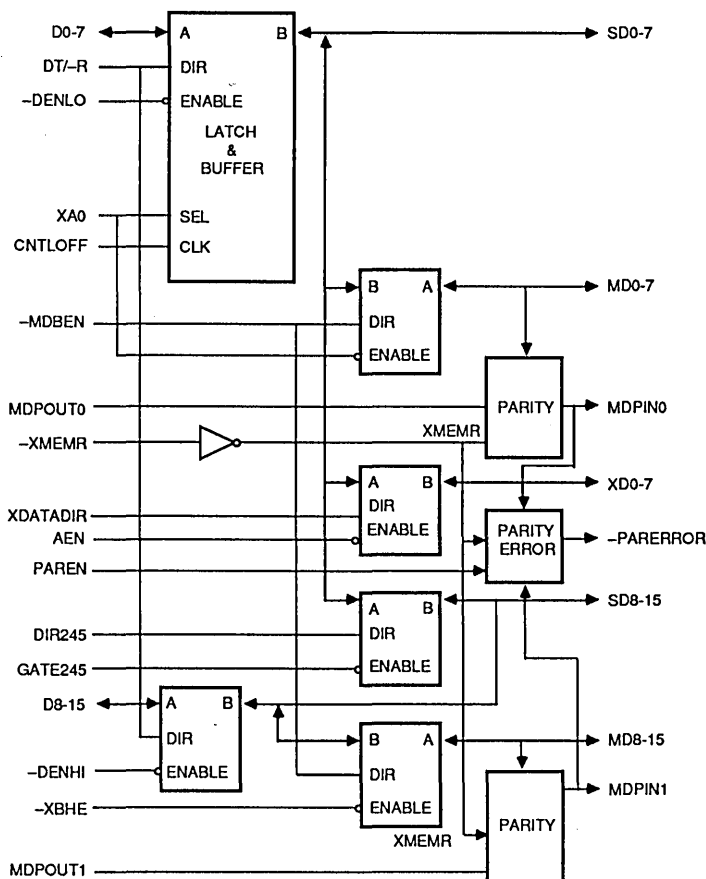
- Fully compatible with IBM PC/AT-type designs
- Completely performs data buffer function in IBM PC/AT-compatible systems
- Replaces several buffers, latches and other logic devices
- Supports 12 MHz processor clock
- Device is available as "cores" for user-specific designs
- Designed in CMOS for low power consumption

## DESCRIPTION

The VL82C104 PC/AT-Compatible Data Buffer provides a 16-bit CPU data bus I/O as well as 40 buffered drivers. The buffered drivers consist of 16 bidirectional system data bus drivers, each capable of sinking 20 mA (50 'LS loads) of current; eight bidirectional peripheral bus drivers, each capable of sinking 8 mA (20 'LS loads) of current; and 16 memory data bus drivers, each capable of sinking 8 mA (20 'LS loads) of current. The VL82C104 also generates the parity error signal for the system.

The device is manufactured with VLSI's advanced high-performance CMOS process and is available in a JEDEC-standard 84-pin plastic leaded chip carrier (PLCC) package. The VL82C104 is individually available, or may be purchased as part of the complete five device IBM PC/AT-compatible kit.

## BLOCK DIAGRAM



## ORDER INFORMATION

Part Number	Package
VL82C104-QC	Plastic Leaded Chip Carrier (PLCC)

Note: Operating Temperature range is 0°C to +70°C

**PLEASE CONSULT PC/AT-COMPATIBLE USERS MANUAL FOR DETAILED INFORMATION**