

## HiNT CS8005 Configuration Registers (by tisenol00)

Warning: Register documentation may be inaccurate!

### HiNT CS8005:

#### Register 02h:

Bit 4-3-2: Bus Clock (1 = CPU Bus Speed)

0 0 0	1/10
0 0 1	1/8
0 1 0	1/6
0 1 1	1/5
1 0 0	1/4
1 0 1	1/3
1 1 0	1/2
1 1 1	1/1

#### Register 03h:

Bit 3: Slow Refresh (1: 1/8 Rate/0: Normal)

Bit 2: I/O Recovery Time (1: ASAP/0: 11 BUS CLKs)

#### Register 1Ah:

Bit 5: C8000-CFFFF Shadow Write Enable

Bit 4: C8000-CFFFF Shadow Read Enable

Bit 1: C0000-C7FFF Shadow Write Enable

Bit 0: C0000-C7FFF Shadow Read Enable

#### Register 1Bh:

Bit 5: D8000-DFFFF Shadow Write Enable

Bit 4: D8000-DFFFF Shadow Read Enable

Bit 1: D0000-D7FFF Shadow Write Enable

Bit 0: D0000-D7FFF Shadow Read Enable

#### Register 1Ch:

Bit 5: F0000-FFFFF Shadow Write Enable

Bit 4: F0000-FFFFF Shadow Read Enable

Bit 1: E0000-EFFFF Shadow Write Enable

Bit 0: E0000-EFFFF Shadow Read Enable