



Intel[®] 865G/865GV865PE/865P Chipset

CRB Schematics

*Addendum for the Intel[®] Pentium[®] 4 Processor on 90 nm Process
with Loadline A Platforms – 2 Phase VR*

February 2004

Revision 1.0



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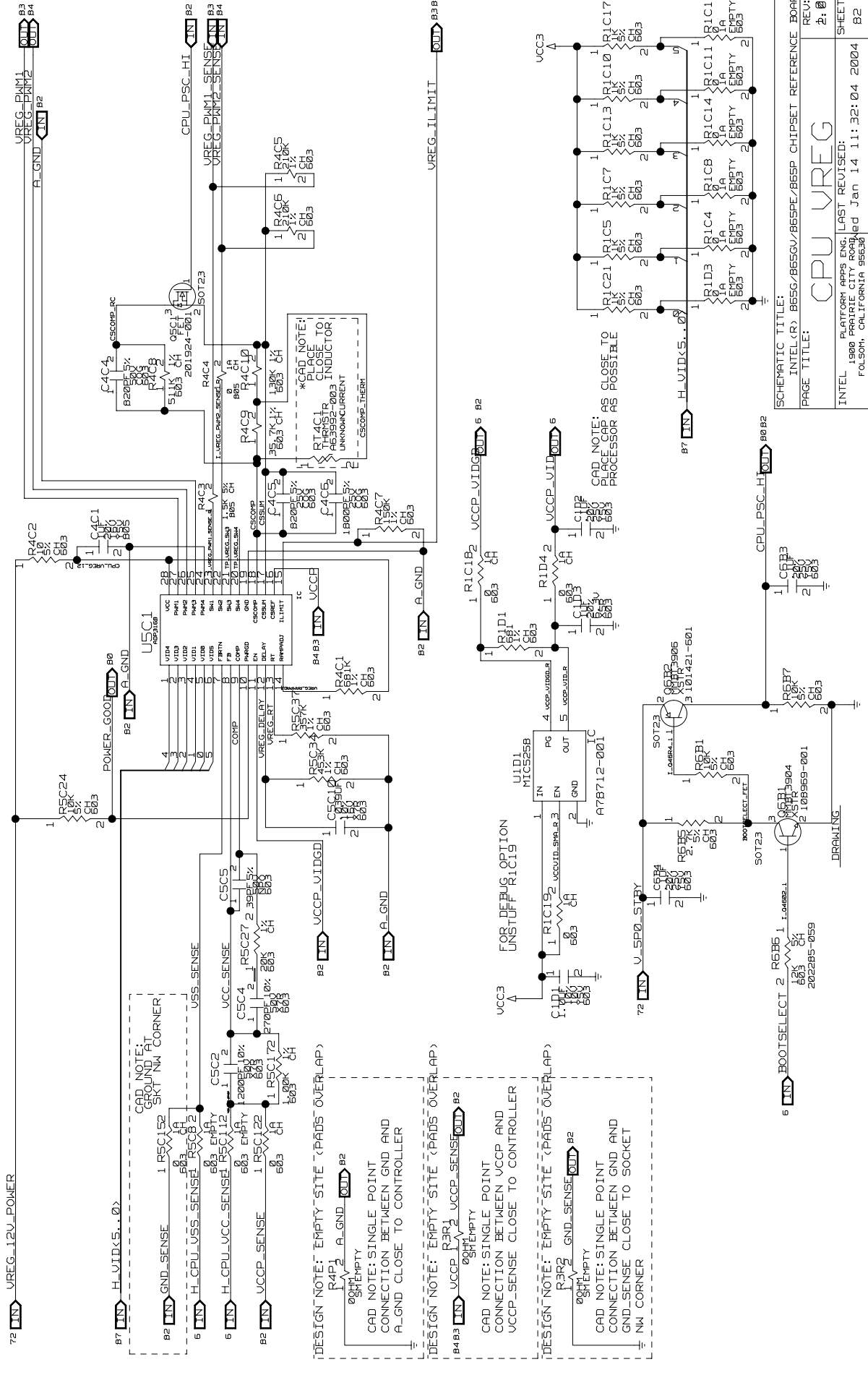
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D	<p>INTEL(R) 865G/865GV/865P/865PE CHIPSET CUSTOMER REFERENCE BOARD SCHEMATICS ADDENDUM FOR THE INTEL(R) PENTIUM(R) 4 PROCESSOR ON 90 NM PROCESS WITH LOADLINE A PLATFORMS</p> <p style="text-align: center;">-2 PHASE VR</p> <table border="1"> <tr> <td>PAGE #</td> <td>COMPONENT/FUNCTION</td> </tr> <tr> <td>1</td> <td>INDEX PAGE AND LEGAL DISCLAIMER</td> </tr> <tr> <td>2</td> <td>BLANK</td> </tr> <tr> <td>3, 4, 5, 6</td> <td>CPU VREG</td> </tr> </table>							PAGE #	COMPONENT/FUNCTION	1	INDEX PAGE AND LEGAL DISCLAIMER	2	BLANK	3, 4, 5, 6	CPU VREG	C
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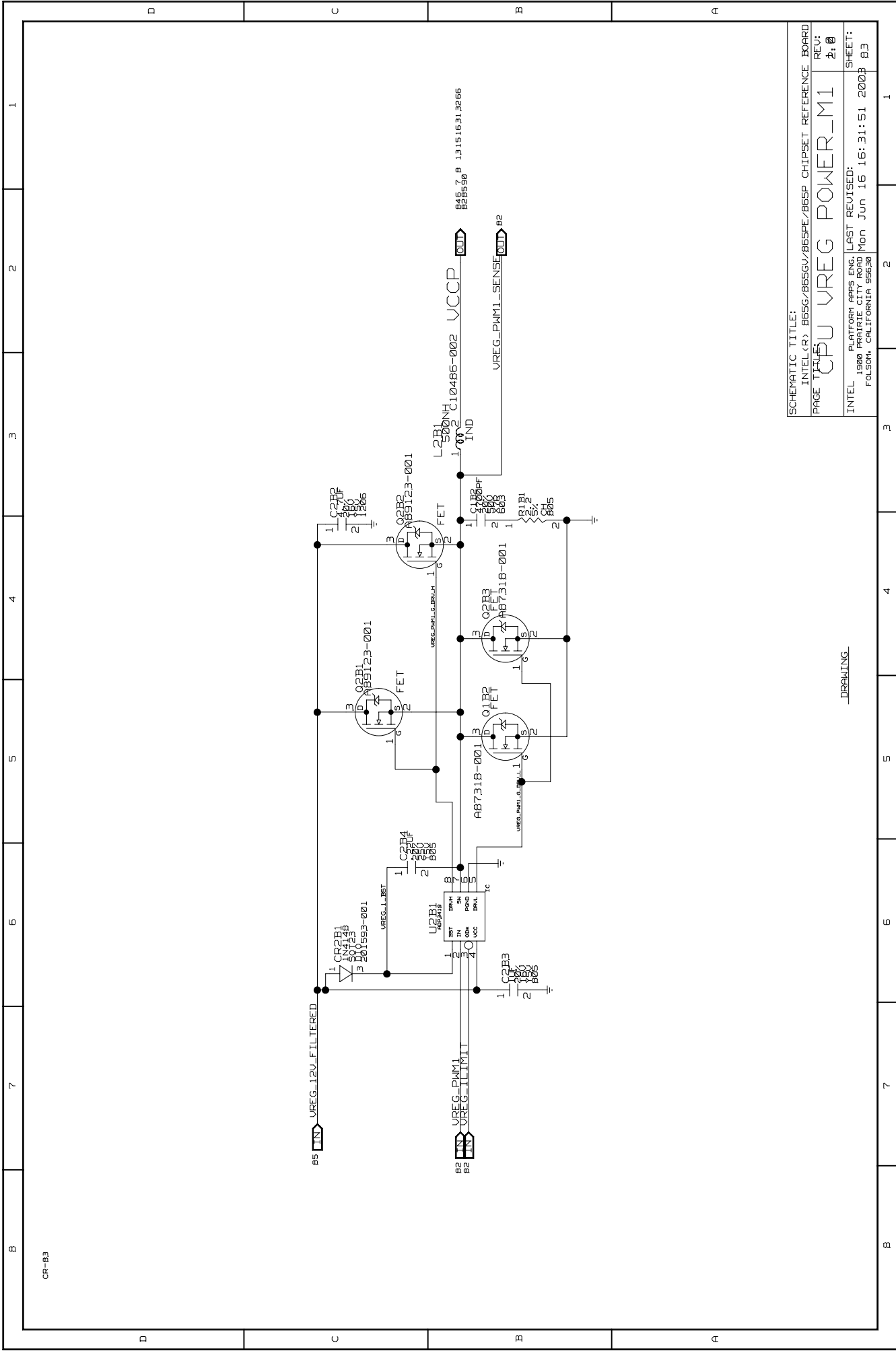
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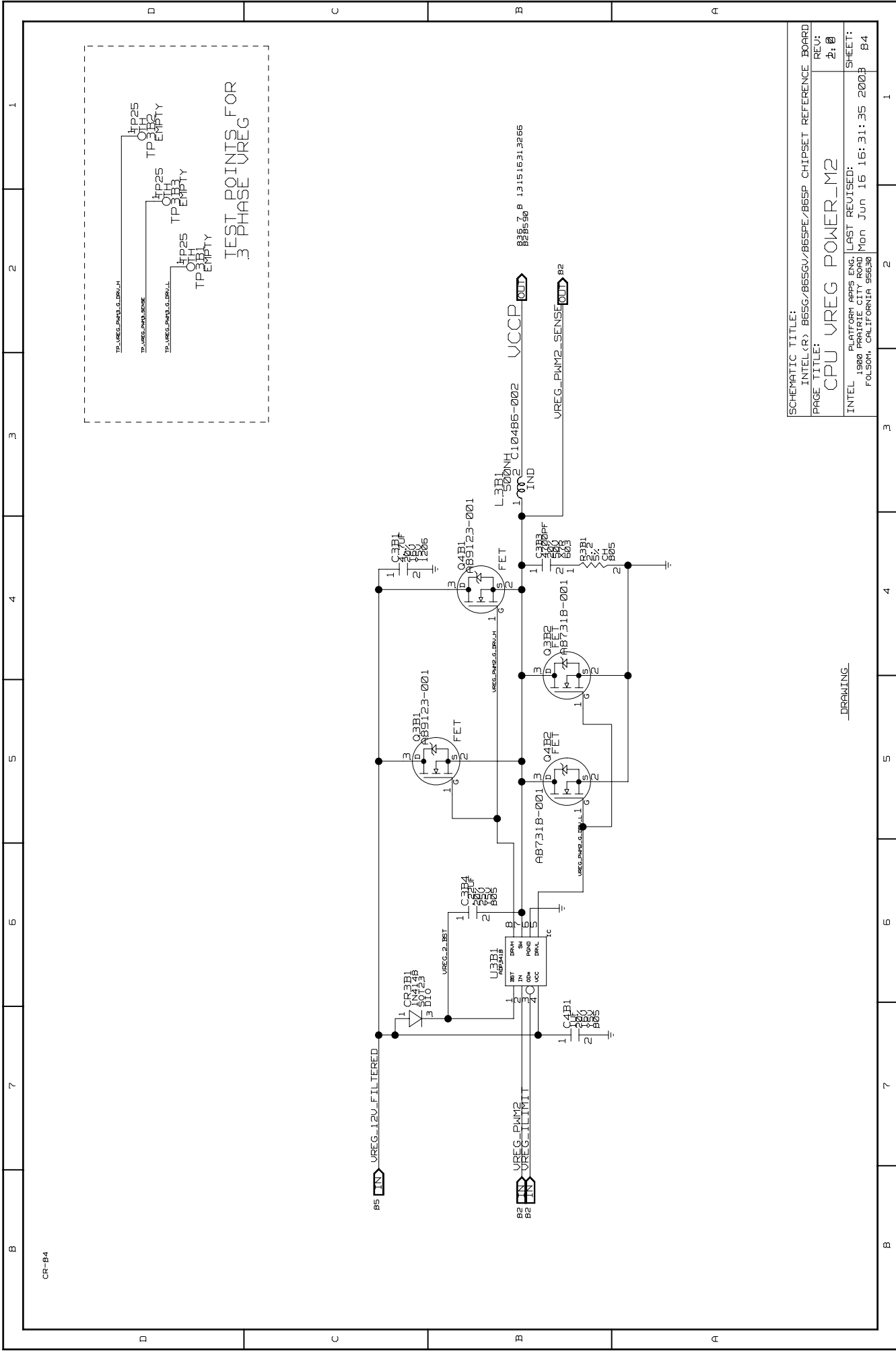


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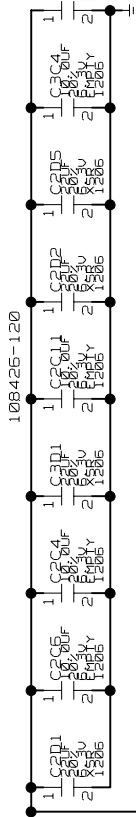


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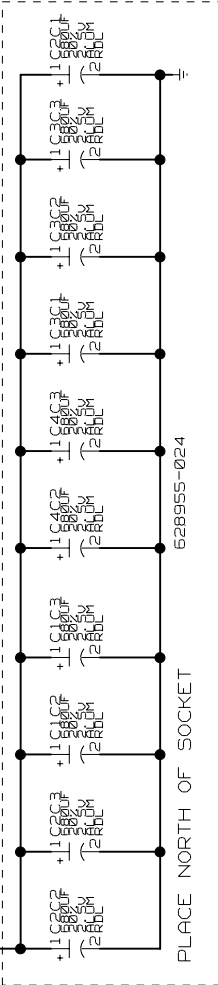
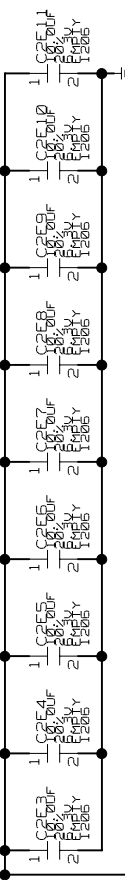
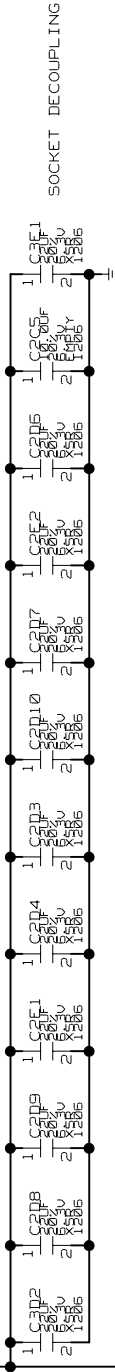
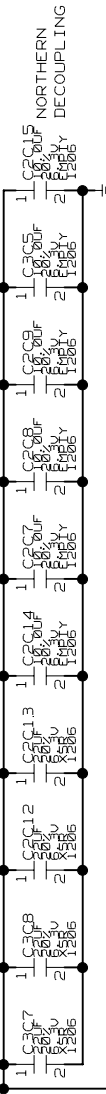
CAD NOTE: PLACE AS MANY 1206 CAPACITORS AS POSSIBLE WITHIN CPU CAVITY

B483 UCCP



PLACEMENT NOTE FOR 1206:
 PLACE 19 NORTH OF SOCKET (STUFF 4)
 PLACE 12 INSIDE CPU SOCKET (STUFF ALL)
 PLACE 9 SOUTH OF SOCKET (UNSTUFF ALL)

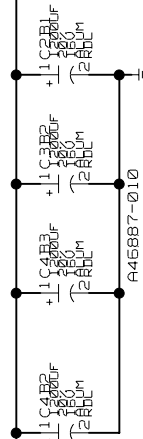
CAPS CHANGED TO 22UF
 644066-024 FOR CRB SKU ONLY



PLACE NORTH OF SOCKET

72 UREG_12V_POWER 200L
 L4B1
 100L
 AB3534-001

UREG_12V_FILTERED B384



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